Claims

1. A digital broadcasting receiving apparatus, having a receiving circuit for receiving a transmitted high frequency signal of a plurality of time division multiplexed programs, comprising a circuit for selecting a better receiving characteristic condition while a desired program is not received.

2. The digital broadcasting receiving apparatus according to Claim 1, wherein the circuit for selecting a better receiving characteristic condition is a parameter switch timing generating circuit.

3. The digital broadcasting receiving apparatus according to Claim 2, wherein the parameter switch timing generating circuit is arranged to switch over antenna characteristics.

4. The digital broadcasting receiving apparatus according to Claim 3, wherein the antenna characteristic comprises two different polarized wave surface antennas, which are arranged to be switched each other.

5. The digital broadcasting receiving apparatus

according to Claim 1, wherein the circuit for selecting a better receiving characteristic condition is an operation starting point controlling circuit for changing an operation point of a variable gain circuit.

6. A digital broadcasting receiving apparatus comprising:

an antenna portion comprising a first antenna and a second antenna for receiving a signal of a plurality of modulated and time division multiplexed programs and a second antenna;

a switching circuit for selecting one of the first antenna and the second antennas on the basis of a level of a switching signal;

a channel-selecting portion for selecting a desired program among the plurality of programs;

a receiving portion for converting the time base multiplexed signal selected in the switching circuit into a base band signal;

an electric field strength detector for detecting electric field strength of the time division multiplexed signal received at the antenna portion;

a power supplying circuit for supplying the receiving portion with power;

power supply controlling means for generating a signal turning on and off the power supplying circuit; and

switch controlling means for generating the switching signal,

wherein the power supply controlling means generates a signal for turning on the power supply in a first period in which a program selected by the channel-selecting portion is multiplex and in a second period corresponding to a part of a multiplexed program preceding to the program selected by the channel-selecting portion, the signal turning off the power supply in a third period other than the above; and

wherein, in the second period, the switch controlling means obtains a first electric field strength from the electric field strength detector, inverts a signal level of the switching signal to obtain a second electric field strength from the electric field strength detector, and returns the signal level of the switching signal to an original level when the second electric field strength is larger than the first electric field strength.

7. The digital broadcasting receiving apparatus according to Claim 6,

wherein the first period and the second period are consecutive.

8. The digital broadcasting receiving apparatus according to Claim 7, further comprising an automatic gain

controlling circuit for making amplitude of the base band signal constant,

wherein the electric field strength detector outputs the first and second electric fields strength on the basis of a gain controlling signal generated in the automatic gain controlling circuit.